

A3

8. (Amended) The method according to claim 1, wherein the identifier is a section of a program identifier which identifies the respective information.

Sub B1

A4

12. (Amended) A method of reprogramming information in a memory arrangement of a computer, comprising the step of:

selecting an identifier from the information entered into an area of the memory to be erased and/or programmed, the identifier identifying a correct erasing and/or programming of the memory arrangement.

Sub B1

A5

14. (Amended) The method according to claim 12, further comprising the step of: selecting the identifier from the information entered into a further area of the memory arrangement, the further area being erased and/or programmed only after erasing and/or programming of the area.

Sub B1

A6

16. (Amended) The method according to claim 12, further comprising the step of: altering the selected identifier in the memory arrangement before erasing and/or programming the information.

Sub B1

A7

18. (Amended) The method according to claim 12, further comprising the step of: selecting the identifier as at least one section of a predetermined length of the information entered into the memory arrangement.

Sub B1

A8

20. (Amended) The method according to claim 12, wherein the identifier is a section of a program identifier which identifies the information.

Sub B1

A9

24. (Amended) A device for erasing and/or programming information in a memory arrangement of a computer, comprising:
a programming arrangement entering an identifier into an area of the memory arrangement to be erased and/or programmed, the identifier identifying a correct erasing and/or programming of the memory arrangement, the programming arrangement altering the identifier in the memory arrangement before erasing and/or programming the information.

Sub B1

A10

27. (Amended) A device, comprising: